

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	4152	(fpga pla pld (programmable adj logic)) and counter and decoder	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/01 18:58
2	BRS	L2	2465	1 and interconnect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/01 18:59
3	BRS	L4	119	2 and macrocell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/01 19:00
4	BRS	L3	100	2 and decoder near3 synchroniz\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/01 19:01
5	BRS	L6	219	3 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/01 19:01